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* Project2 Part A - Sinusoidal tone generator
                This code plays the musical octave continuously (ie.
from middle C to upper C)
* Written by: Thusitha Mabotuwana
          9790416
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      ORG
             $4100
                        ; Look up table for tones where
$82=Middle C,$7B=C\#,$74=D,$6D=D+ etc.
        FCB $82,$7B,$74,$6D,$67,$61,$5B,$56
        FCB
              $51,$4D,$49,$42,$41
      ORG $4000
                          ; Given sampling values to generate one
cycle of the sine wave
        FCB $80,$9F,$CB,$E8,$FA,$FF,$FA,$E8,$CB
              $9F,$80,$59,$35,$19,$6,$0,$6,$18,$34,$58
*_____
N EQU $14 ;20 samples
TOC1 EQU $1016 ;Time Output Compare 1
TMSK1 EQU $1022 ;Timer Mask1
TFLG1 EQU $1023 ;Timer Flag1
FREQ EQU $3900 ;The tone to pick up is stored in this
memory location.
LKUP EQU $4000 ;Start address of sample lookup table CYCLES EQU $4300 ;The number of times the counter has
access the whole of memory is sorted in this address
LED EQU $A201 ;address of LED2

JP8 EQU $A400 ;Port direction

PORT1 EQU $A600 ;PLD Port1 address
      ORG $3CB
                      ;Start address of ISR. Interrupt occurs
when TCNT=TOC1.
       JMP ISR
        ORG $2000 ;Start of the program
          CLRA
                                    ; Initialisation of regiters to make
them 0 at first
      LDX #0
                 ;Need to make acc LED displays zero
        STAA FREQ
            STAA TFLG1
      STX LED
LDAB #01
                        ;Displays 0000 at the start
                        ;Initial display should be 0001.Need manual 01
since this is 00 in machine
      STAB $A201
        STAA JP8
                               ;Set PLD PORT1 direction as OUTPUT
                              ;Initially CYCLES is zero
        STAA CYCLES
        LDAA #$80
                               ;Write #1000 0000 to TMSK1 to enable
TOC1
           STAA TMSK1 ;Store $80 in TMSK1 at address 1022
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BEGIN	CLI LDX LDY	#N #LKUP	;Enable interrupts to occur ;Load the number of samples ;Y Points to the first SAMPLE in the
Lookup LOOP1 table	table LDAA	0,Y	;LOAD A with CURRENT sample from Lookup
	STAA INY	PORT1	;OUTPUT DATA to PORT1;INC index Y to be ready for NEXT
	JSR DEX	T1	;Call DELAY subroutine ;DEC X
cycle s	BEQ starts	BEGIN	;IF X=0, ONE Cycle finishes and next
4	JMP	LOOP1	;Otherwise, OUTPUT next SAMPLE
	SUBRUTI	NE:	Record Y in a Stack so the register
	used in	this subroutine	_
the ton	LDY ie lookup	#\$4100	;Load Y with the starting address of
che con		FREQ	;Load ACCB with tone number
of the	ABY	kup table	;Add it to Y to get the current address
or the		kup tabie \$0,Y	Get current tone repeat value
	DECA	, .	start time delay loop to get the
correct	time de		
	BNE PULY	L1	;Recall Y from Stack before returning
to main	program		/Recall I from Stack Derore returning
	RTS		Return from Subroutine
*			
* Interrupt Service Routine			
	_		;It takes 0.016384 seconds to access
ISR all mem	LDD nory loca		/IL Lakes 0.010304 Seconds to access
ADDD TOC1 ;add \$FFFF to TOC1, so after 0.016384 sec TCNT=TOC1 causing interrupt			
whole o	STD cycle fin		Stored in TOC1 so as to check when next
LDAA CYCLES ;ACCA has the number of times whole of			
memory (65535) has been accessed INCA ;Increase number of times when			
interrupt occurs			
		CYCLES ;	Updated value stored ; Whole of memory need to be accessed
244 tim		4s delay.	;Less than 244 times. Therefore should
play th	ie same n	ote	
move to	LDAA the nex	FREQ t tone	;Memory accessed 244 times. Need to
I	INCA	;Update	d frequency

; Initially 1 was loaded(to avoid displaying 0000) thus when cycle is 2, 2 has to be displayed. STAA LED ; and display result DECA ;Restore value in ACCA STAA FREQ ;Updated frequency stored ; Check if all 13 noted have been played CMPA #13 START BNE ;Should start playing notes from the beginning. (ie again from mm \$4100) ;Start from the beginning CLRA STAA FREQ ;Updated tone stored LDAA #01 STAA LED ;Display 0000 on LED's START CLRA ; End of cycles for 1 tone. Start from the beginnning for next tone STAA CYCLES ;Updated value of CYCLES stored

DONE LDX #\$1000 ; Needed for BCLR

BCLR TFLG1,X \$7F ;This need to be cleared to enable

interrupt in the next cycle

RTI ;Return from ISR

END